

SERIAL NO.

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GROUP

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| | Paper entitled "Submission of Prior Art Under 37 CFR 1.501, 6 pages. 3-22-2007. |
| 1. | Presentation slides entitled <u>System-on-chip (SOC) and its Effect on Microcomputer Bus Products</u> , as presented to the VMEbus Standards Organization (VSO) on July 21, 1999, in Vancouver, B.C., Canada, 31 pages. |
| 2. | Technical reference manual entitled <u>VME64 to PCI Bridge System-on-Chip (SoC)</u> , first published by Silicore Corporation on December 7, 2002, 129 pages. |
| 3. | VHDL source code files representing the VMEcore entity and testbench, as produced by Silicore Corporation's parametric core generator known as <u>The VMEbus Interface Writer™</u> on February 14, 2002, 46 pages. |
| 4. | VHDL source code files representing the VMEcore entity as distributed with the <u>VME64 to PCI Bridge System-on-Chip (SoC)</u> , and described at §4.7.3 <i>VHDL Synthesis and Test</i> (p. 81) in the technical reference manual, 31 pages. 2-14-2006. |
| 5. | <u>WISHBONE System-on-Chip (SoC) Interconnection Architecture for Portable IP Cores</u> , Revision B.2, dated October 10, 2001, 109 pages. |
| 6. | Press release from Altium Ltd. (Sydney, Australia) entitled <u>Altium Unveils new 'Board-on-Chip' Technology</u> , dated April 28, 2003, 3 pages. |
| 7. | Press release from Altium Ltd. (Sydney, Australia) entitled <u>Altium Introduces Systems Focus to FPGA Design with Nexar</u> , dated November 17, 2003, 5 pages. |

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Form PTO-FB-A820 (Also PTO-1449)

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